



Attorney's Docket No: INTEL-005PUS
Intel docket #: P16867

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicants:	Gururaj M. Katti et al.	Art Unit	: 2195
Serial No.:	10/662,242	Examiner	: Michael P. Wilser
Filed	: September 15, 2003	Conf. No.	: 2391
		Assignee	: Intel Corporation
Title	: OPTIMIZING CRITICAL SECTION MICROBLOCKS BY CONTROLLING THREAD EXECUTION		

MAIL STOP AF

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

AMENDMENT UNDER 37 C.F.R. §1.116 IN REPLY TO ACTION OF JANUARY 25, 2008

In accordance with the provisions of 37 C.F.R. §1.116, please amend the above-identified application as follows:

OK TO ENTER: /EW/

02/22/2008